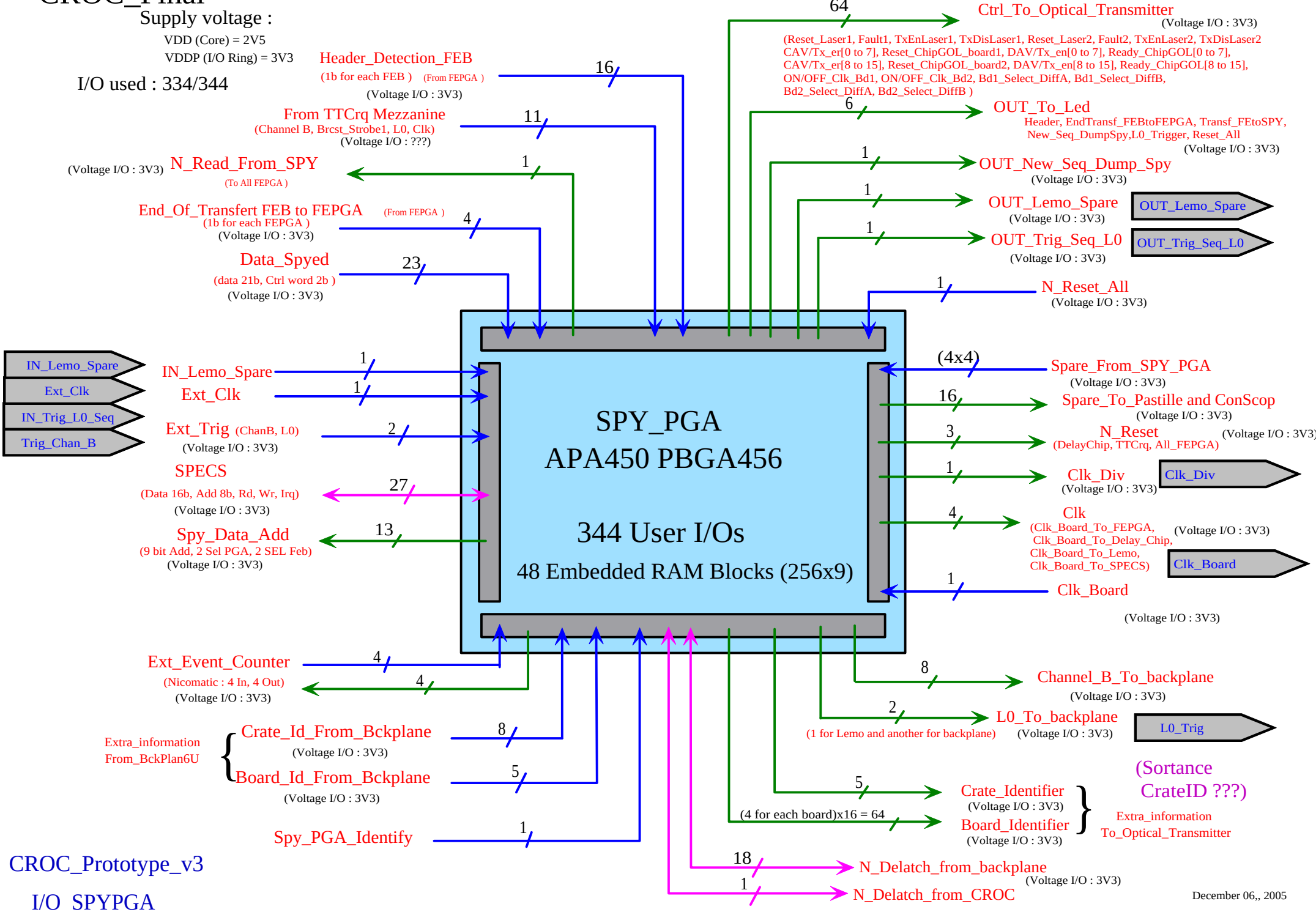
CROC_Final

Supply voltage :

VDD (Core) = 2V5

VDDP (I/O Ring) = 3V3

I/O used : 334/344



CROC_Prototype_v3

I/O SPYPGA

December 06,, 2005